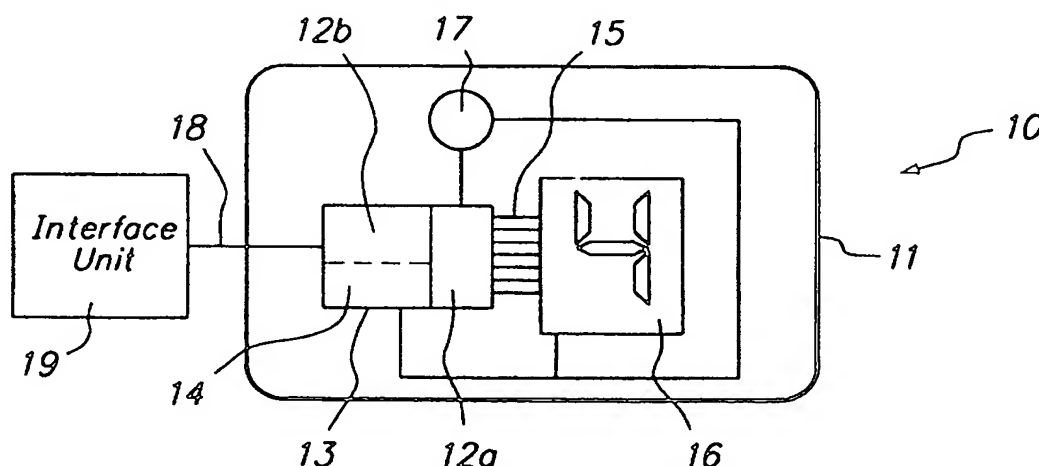




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁷ : G06K 19/07		A1	(11) International Publication Number: WO 00/03353
			(43) International Publication Date: 20 January 2000 (20.01.00)
(21) International Application Number: PCT/US99/15481 (22) International Filing Date: 8 July 1999 (08.07.99) (30) Priority Data: 09/112,497 9 July 1998 (09.07.98) US (71) Applicant: TYCO ELECTRONICS CORPORATION [US/US]; 300 Constitution Drive, Mail Stop 120/1A, Menlo Park, CA 94025-1164 (US). (72) Inventors: GAUTHIER, Dominique; 60, rue Victor Hugo, F-92300 Levallois-Perret (FR). MUNCH, Mark, R.; 850 Rorke Way, Palo Alto, CA 94063 (US). TOMITA, Akira; 929 Whitehall Lane, Redwood City, CA 94061 (US). GOODELL, John, L.; 1608 Redwing Avenue, Sunnyvale, CA 94087-5041 (US). GOZLAN, Gilles; 2, rue du Bas Mesnil, F-60240 Le Mesnil Theribus (FR). (74) Agents: CHAO, Yuan et al.; Tyco Electronics Corporation, Intellectual Property Law Dept., 300 Constitution Drive, MS 120/1A, Menlo Park, CA 94025-1164 (US).		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, VN, YU, ZW, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i>	

(54) Title: INTEGRATED CIRCUIT CARD WITH LIQUID CRYSTAL DISPLAY FOR VIEWING AT LEAST A PORTION OF THE INFORMATION STORED IN THE CARD



(57) Abstract

An integrated circuit card (10) contains a liquid crystal display (16) for displaying at least a portion of the information stored therein. The displayed information is stored in a first, or reserved, memory (12A). A second, or additional, memory (12B) is used to store further information. The use of a reserved memory for storing the displayed information permits the use of very low power electrical energy sources (17) for driving the display and the electronics controlling the pixels of the display, such as piezoelectric sources, pyroelectric sources, photovoltaic cells, and very small batteries. The first and second memories may be integrated into a single IC chip, or they may be separate components.

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece			TR	Turkey
BG	Bulgaria	HU	Hungary	ML	Mali	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MN	Mongolia	UA	Ukraine
BR	Brazil	IL	Israel	MR	Mauritania	UG	Uganda
BY	Belarus	IS	Iceland	MW	Malawi	US	United States of America
CA	Canada	IT	Italy	MX	Mexico	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NE	Niger	VN	Viet Nam
CG	Congo	KE	Kenya	NL	Netherlands	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NO	Norway	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	NZ	New Zealand		
CM	Cameroon			PL	Poland		
CN	China	KR	Republic of Korea	PT	Portugal		
CU	Cuba	KZ	Kazakstan	RO	Romania		
CZ	Czech Republic	LC	Saint Lucia	RU	Russian Federation		
DE	Germany	LI	Liechtenstein	SD	Sudan		
DK	Denmark	LK	Sri Lanka	SE	Sweden		
EE	Estonia	LR	Liberia	SG	Singapore		

INTEGRATED CIRCUIT CARD WITH LIQUID CRYSTAL DISPLAY FOR VIEWING AT LEAST A PORTION OF THE INFORMATION STORED IN THE CARD

TECHNICAL FIELD OF THE INVENTION

5 This invention relates to an integrated circuit card having a liquid crystal display for viewing at least a portion of the information stored therein.

BACKGROUND OF THE INVENTION

Integrated circuit (IC) cards are finding increasing popularity as compact, highly portable information storage articles. Also known as smart
10 cards or chip cards, IC cards typically are credit card sized and contain an integrated circuit element that provides electronic data storage and/or logic function capability. Information stored on an IC card can be widely ranging, from financial to medical to personal identification information. Additionally, IC cards usually contain input/output connections for connection to an
15 external control device that retrieves, updates, and/or displays the stored information.

A drawback with most IC cards is that the stored information is not immediately viewable by the bearer. While the IC card may contain information on the bearer's bank account balance, the bearer may not be able
20 to see conveniently what is that balance. One solution has been to provide a separate IC card reader that "reads" the stored information and displays it. But such a solution has the limitation of requiring an additional component that may not be immediately accessible or readily portable, for example requiring the bearer to go to the nearest bank branch or ATM machine or to
25 remember to bring the reader along on a trip.

Some constructions in which a display is integrated into an IC card have been proposed, such as Jachimowicz et al., US 5,734,154 (1998) entitled "Smart Card With Integrated Reader And Visual Image Display." As IC cards increase in popularity and find applications in more and more areas of daily

life, bearer access to the stored information becomes increasingly important. At the same time, their ubiquitousness means that IC cards are approaching commodity item status, meant to be disposed of after a finite number of uses, imposing strong economic pressure reduce their cost. Further, satisfying size, weight, and ruggedness requirments permitting an IC card to be carried around in a wallet is an important consideration. Lastly, due to its limited size and thickness, an IC card offers limited electrical power storage (or generation) capability, so that the power consumption of a display and the electronics associated with it must be low enough.

10 BRIEF SUMMARY OF THE INVENTION

Thus, it is an object of this invention provides an IC card having an integrated display for viewing at least a portion of the information stored therein, where the display and associated electronics have low power consumption. Another object of this invention is to provide an IC card capable of displaying such stored information autonomously. An IC card of this invention preferably comprises a display employing a liquid crystal composite (as described further hereinbelow) as electro-optically active material, such material being especially adaptable for use in combination with plastic substrates and leading to a flexible and rugged IC card. These and other objects are achieved by an integrated circuit card according to this invention, comprising:

- a. a card-shaped support;
- b. a reprogrammable nonvolatile first memory on the support, for storing information to be displayed;
- 25 c. an integrated circuit chip on the support, comprising
 - i. a reprogrammable nonvolatile second memory for storing additional information; and
 - ii. a serial port on said support for interfacing the integrated circuit chip with an external controller device; and

- d. a liquid crystal display on the support, the liquid crystal display having plural pixels wherein, when the liquid crystal display is driven, the optical state of each pixel is determined by the state of a corresponding bit of information stored in the first memory;
- 5 e. plural parallel output ports connecting each pixel of the liquid crystal display to the corresponding bit of information stored in the first memory; and
- f. an electric energy source on the support, for driving the first memory and the liquid crystal display, the electrical energy source driving the first memory without affecting information
- 10 stored in the second memory.

BRIEF DESCRIPTION OF THE DRAWING(S)

Fig. 1 shows schematically an IC card of this invention.

Figs. 2a, 2b, and 2c show electric energy sources for an IC card of this invention.

15

Reference numerals repeated from one figure to another denote the same or like elements.

DETAILED DESCRIPTION OF THE INVENTION

An integrated circuit (IC) card 10 of this invention is shown in Fig. 1, which is typically credit card sized, although its size is not especially critical and it may be somewhat larger or smaller. Preferably, IC card 10 is less than 100 mm wide (more preferably less than 90 mm wide), less than 70 mm high (more preferably less than 60 mm high), and less than 2 mm (more preferably less than 1 mm) thick. Typical ISO dimensions for an IC card are 85.5 x 54 x 0.76 mm. IC card 10 comprises a card shaped support 11 on which the other elements of IC card 10 are supported, for example by being partially or wholly embedded therein or by being surface mounted thereon. Support 11 may be made of flexible plastic, for example by molding or lamination.

20

25

On support 11 is a reprogrammable nonvolatile first memory 12a, which may be, for example, an EEPROM (electrically erasable programmable read-only memory) or a flash memory. Also on support 11 is an IC chip 13 including a reprogrammable nonvolatile second memory 12b. IC chip 13 is not restricted to being a memory-only chip; it may additionally have optional logic circuits 14 for performing calculations on the stored data. First memory 12a is a reserved memory, for storing the information to be displayed by IC card 10. If IC card 10 is used, by way of illustration, for a bank account, the information in first memory 12a may be key information such as the account balance. Second memory 12b, normally larger in size than first memory 12a, may store additional, less key information (such as, continuing the illustration, recent deposits and withdrawals, interest earned, etc.). The information stored in first memory 12a may be redundantly stored in second memory 12b, or the information stored in first and second memories 12a, 12b may be non-overlapping, i.e., mutually exclusive. IC chip 13 includes a serial input/output (I/O) port 18 for communicating with an external interface unit 19 (also known in the art as a card reader/writer), for updating the information stored in first and second memories 12a, 12b. Alternatively, the information destined for first memory 12a can be stored in second memory 12b first (or computed there, if IC chip 10 is equipped with logic circuits 14) and then transferred to first memory 12a. Communication between IC card 10 and interface unit 19 may be with contact or without contact, for example through an RF antenna system. Most existing protocols, including ISO 7816, may be used for communication between IC card 10 and interface unit 19.

Plural parallel ports (outputs) 15 link first memory 12a with a liquid crystal display 16, one port per pixel of the latter. A local electrical energy source 17 powers IC chip 13 and liquid crystal display 16. In a preferred embodiment, local energy source powers 17 only first memory 12a.

Alternatively, local electrical energy source 17 powers IC chip 13, but only first memory 12a is in the "read" mode, while the rest of IC chip 13 is in the

stand-by mode. According to the state of each bit of information in first memory 12a (0 or 1), the corresponding pixels in liquid crystal display 16 will be powered on or not, thus causing a pattern to be displayed. Segregation of the displayed information in memory 12a prevents the information of second memory 12b from being corrupted accidentally.

It is to be understood that the terms "first" and "second" memories are used to identify which of the memory components in integrated circuit card 10 have a parallel port 15 connected thereto or not. There are no rigid numerical limitations on the (digital) size of memories 12a, 12b, but first memory 12a should have at least as many memory cells as there are parallel ports 15. First memory 12a may be separate from IC chip 13 — i.e., a separate component on card 10 distinct from IC chip 13 — or it may be integrated therewith. Further, where integrated, first memory 12a may be "dispersed" within second memory 12b or it may occupy a segregated block within IC chip 13.

Because the information content of first memory 12a is limited, very little power is required to drive the system. When IC card 10 is in communication with interface unit 19, interface unit can provide more power, driving the entire IC chip 13, and information on both first and second memories 12a, 12b can be addressed. Thus, an IC card of this invention permits substantial amounts of information to be stored, with key portions thereof always available for viewing by the bearer, even when the card is in its autonomous mode, i.e. not in communication with another device or an external power source. When the card is connected to interface unit 19, or is inserted into a separate reader unit (not shown) having more power, all the information stored in the IC card may be viewed on an appropriate display (liquid crystal, electrochromic, plasma, etc.) associated with interface unit 19 or the reader unit. Driving a liquid crystal display with conventional driver IC's requires significant electrical power. In the invention, driving liquid crystal display 16 via a memory chip alone (or a memory component alone of

a logic-plus-memory chip) allows one to reduce drastically power consumption, thus enabling the use of a much smaller power source. Moreover, by bifurcating the total information stored and the information displayed in the autonomous mode, power consumption can be further
5 reduced.

Besides requiring very little power, the present invention is advantageous in its simplicity. No separate driving circuit is required; rather, first memory 12a and liquid crystal display 16 are directly driven by electric energy source 17. The absence of a need to power a separate driving circuit
10 also contributes to reduced power consumption. The minimal power requirements mean a wide range of electric energy sources 17 can be used with IC card 10, including power sources incompatible with IC cards not according to this invention. Such "local" power sources include piezoelectrically actuated energy sources, pyroelectric energy sources,
15 batteries, and solar cells.

Fig. 2a shows the circuit diagram of an exemplary piezoelectric energy source 20 that can be used as an electric energy source 17, comprising a piezoelectric element 21, a rectifier 22, and a capacitor 23. Electric energy is generated by inputting mechanical work energy, for example by pressing
20 down on a diaphragm made of piezoelectric material. Suitable piezoelectric elements 21 include piezoelectric diaphragms of the external drive/self drive type available from Murata Manufacturing Co., Ltd., of Kyoto, Japan (<http://www.murata.co.jp/>), for instance as a 25 mm disk of piezoelectric material on a brass disk (product no. VSB41D25-07AR0, 140 nF capacitance).
25 Rectifier 22 may be a rectifier bridge such as Multicomp DB 103G. Capacitor 23 may be a 0.1 μ F metallized polyester film capacitor (Philips, no. 222237011104). The rectifier 22/capacitor 23 combination serves to supply first memory 12a and liquid crystal display 16 with DC voltage. In an alternative embodiment, the rectifier bridge may be replaced by a single
30 diode. In yet another alternative embodiment, capacitor 23 may be omitted.

In yet another alternative embodiment, piezoelectric element 21 may be directly connected to first memory 12a, if the latter supports reverse polarity.

Fig. 2b illustrates an alternative electric energy source 17, in the form of a battery 25, which can be for example a lithium polymer battery or a button cell. In such instance, a switch 26 controls the driving (or not) of first memory 12a and liquid crystal display 16. Because of the low power requirements, a much smaller battery can be used, compared to other designs.

Fig. 2c shows yet another alternative electric energy source 17, in the form of a photovoltaic (solar) cell 27, also controlled by a switch 26. The latter is optional, because a photovoltaic cell, unlike a battery, is not susceptible to being drained.

Where electric energy source 17 is a pyroelectric cell, it may be based on certain piezoelectric materials that also display a pyroelectric effect or on thermocouples displaying a Seebeck effect.

As noted above, first memory 12a may be an EEPROM, for example product no. 84041P available from Xicor, Inc., Milpitas, California (<http://www.xicor.com/>) or product no. 28C16A available from Microchip Technology, Inc., Chandler, Arizona (<http://www.microchip.com/>).

One type of liquid crystal display for which the present invention is especially suitable employs a liquid crystal composite comprising plural volumes (or droplets) of liquid crystals contained (i.e., dispersed, encapsulated, or embedded) within a polymer matrix, as the electro-optically active material. Such composites have been referred to in the art alternatively as encapsulated liquid crystal material, nematic curvilinear aligned phase (NCAP) materials or as polymer dispersed liquid crystal (PDLC) materials. Exemplary disclosures include Ferguson, US 4,435,047 (1984); West et al., US 4,685,771 (1987); Pearlman, US 4,992,201 (1991); Andrews et al., US 5,202,063 (1993); Kamath et al., US 5,233,445 (1993); Reamey, US 5,328,850 (1994); Reamey et al., US 5,405,551 (1995);

Wartenberg et al., US 5,427,713 (1995); Reamey et al., US 5,543,944 (1996); Havens et al., US 5,585,947 (1996); Cao et al., US 5,738,804 (1998); Raychem WO 96/19547 (1996); and Dainippon Ink, EP 0,313,053 (1989); the disclosures of which are incorporated herein by reference. Generally, such composites are
5 light scattering and/or absorbing in the absence of a sufficient electric field (the field-off state), but are substantially light transmissive in the presence of such electric field (the field-on state). Displays made with encapsulated liquid crystal material are especially suitable for the present invention, because they are field-effect devices, consuming very low amounts of power. Also, they
10 can be made by lamination between two pieces of plastic (as opposed to glass) substrates, so that they are flexible and rugged. A membrane switch can be integrated with the display, for example where the electric energy source is a battery.

However, other types of liquid crystal displays should be suitable,
15 including twisted nematic (TN) displays. Generally, any type of liquid crystal display that is amenable to being driven directly and tolerates short term exposure to DC current may be used.

The foregoing detailed description of the invention includes passages that are chiefly or exclusively concerned with particular parts or aspects of
20 the invention. It is to be understood that this is for clarity and convenience, that a particular feature may be relevant in more than just the passage in which it is disclosed, and that the disclosure herein includes all the appropriate combinations of information found in the different passages. Similarly, although the various figures and descriptions herein relate to
25 specific embodiments of the invention, it is to be understood that where a specific feature is disclosed in the context of a particular figure or embodiment, such feature can also be used, to the extent appropriate, in the context of another figure or embodiment, in combination with another feature, or in the invention in general.

Further, while the present invention has been particularly described in terms of certain preferred embodiments, the invention is not limited to such preferred embodiments. Rather, the scope of the invention is defined by the appended claims.

CLAIMS

What is claimed is:

1. An integrated circuit card capable of displaying at least a portion of
5 information stored therein, comprising:
 - a. a card-shaped support;
 - b. a reprogrammable nonvolatile first memory on the support, for storing information to be displayed;
 - c. an integrated circuit chip on the support, comprising
10
 - i. a reprogrammable nonvolatile second memory for storing additional information; and
 - ii. a serial port on said support for interfacing the integrated circuit chip with an external controller device; and
 - d. a liquid crystal display on the support, the liquid crystal display
15 having plural pixels wherein, when the liquid crystal display is driven, the optical state of each pixel is determined by the state of a corresponding bit of information stored in the first memory;
 - e. plural parallel output ports connecting each pixel of the liquid crystal display to the corresponding bit of information stored in
20 the first memory; and
 - f. an electric energy source on the support, for driving the first memory and the liquid crystal display, the electrical energy source driving the first memory without affecting information stored in the second memory.
- 25
2. An integrated circuit card according to claim 1, wherein the liquid crystal display comprises a liquid crystal composite as electrooptically active material.

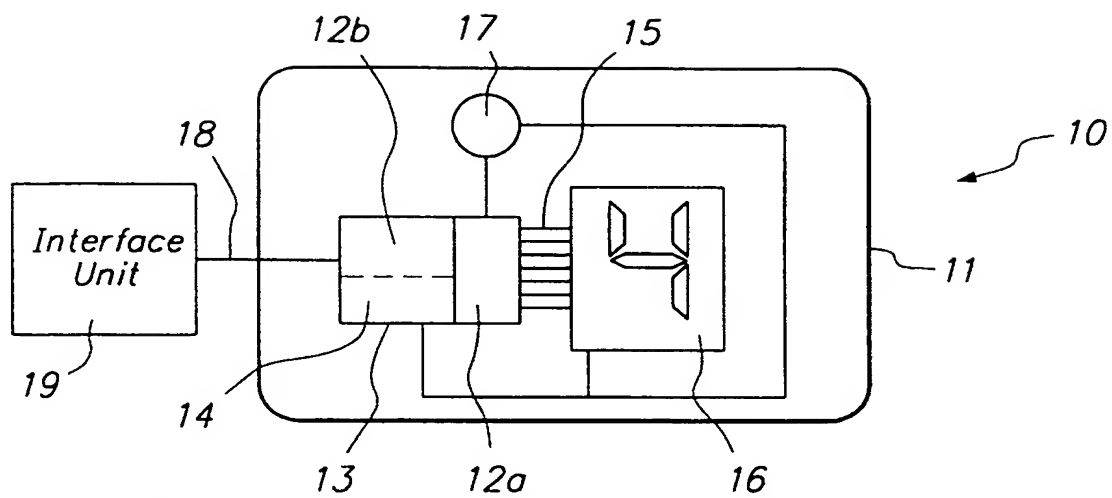
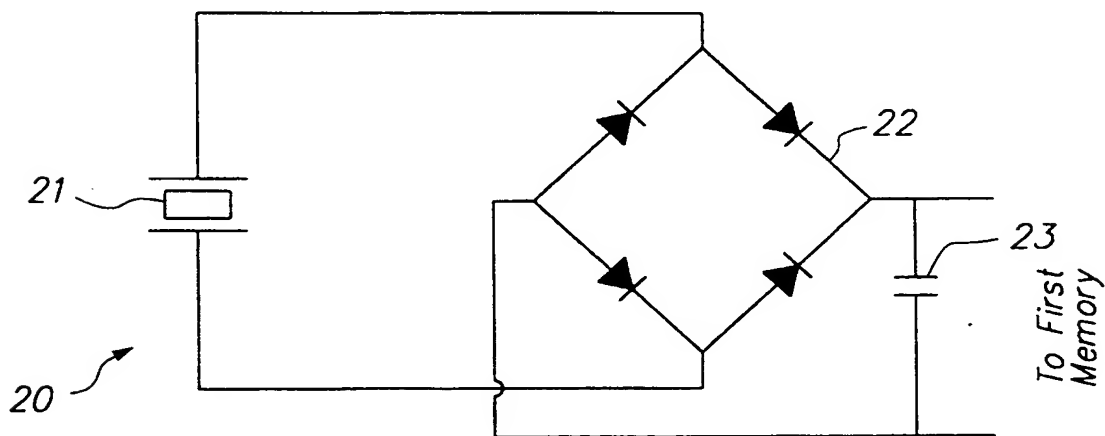
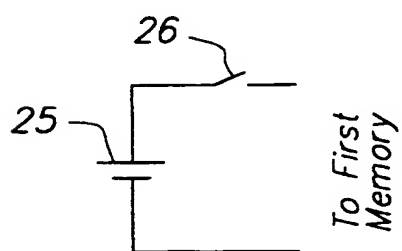
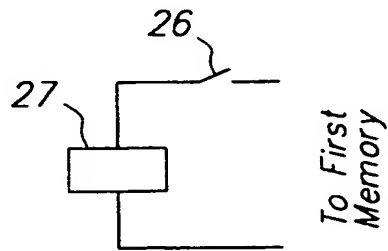
3. An integrated circuit card according to claim 1, wherein the electric energy source is a piezoelectric energy source.
4. An integrated circuit card according to claim 1, wherein the electric energy source is a photovoltaic cell.
5. An integrated circuit card according to claim 1, wherein the electric energy source is a battery.
- 10 6. An integrated circuit card according to claim 1, wherein the electric energy source is a pyroelectric cell.
7. An integrated circuit according to claim 1, wherein the information to be displayed is redundantly stored in the second memory.
- 15 8. An integrated circuit card according to claim 1, wherein the information stored in the first and second memories is non-overlapping.
9. An integrated circuit card according to claim 1, wherein the first memory is an EEPROM.
- 20 10. An integrated circuit card according to claim 1, wherein the first memory is a flash memory.
- 25 11. An integrated circuit card according to claim 1, wherein the integrated circuit chip further comprises logic circuits.
12. An integrated circuit card according to claim 1, wherein the first memory is integrated with the integrated circuit chip.

30

13. An integrated circuit card according to claim 1, wherein the first memory is separate from the integrated circuit chip.
14. An integrated circuit card according to claim 2, wherein the electric energy source is a piezoelectric energy source.
15. An integrated circuit card according to claim 2, wherein the electric energy source is a photovoltaic cell.
16. An integrated circuit card according to claim 2, wherein the electric energy source is a battery.
17. An integrated circuit card according to claim 2, wherein the electric energy source is a pyroelectric cell.
18. An integrated circuit according to claim 2, wherein the information to be displayed is redundantly stored in the second memory.
19. An integrated circuit card according to claim 2, wherein the information stored in the first and second memories is non-overlapping.
20. An integrated circuit card according to claim 2, wherein the first memory is an EEPROM.
21. An integrated circuit card according to claim 2, wherein the first memory is a flash memory.
22. An integrated circuit card according to claim 2, wherein the integrated circuit chip further comprises logic circuits.

23. An integrated circuit card according to claim 2, wherein the first memory is integrated with the integrated circuit chip.

24. An integrated circuit card according to claim 2, wherein the first
5 memory is separate from the integrated circuit chip.

**FIG. 1****FIG. 2a****FIG. 2b****FIG. 2c**

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 99/15481

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G06K19/07

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 291 259 A (SEMICONDUCTOR ENERGY LAB) 17 November 1988 (1988-11-17) column 1, line 47 -column 3, line 16; claims 6,8,14,15 column 5, line 10 -column 6, line 49 column 8, line 33 -column 9, line 9; figures 1A-1C	1-6,8, 11-17, 19,22-24
Y	US 4 918 631 A (HARA KAZUYA ET AL) 17 April 1990 (1990-04-17) column 2, line 51,55; claim 1; figures 2,3 column 3, line 38-42 column 4, line 17-34 column 5, line 20-50 --- -/--	1-7,9, 11-18, 20,22-24

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

28 October 1999

Date of mailing of the international search report

05/11/1999

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Cardigos dos Reis, F

INTERNATIONAL SEARCH REPORT

Intern: al Application No

PCT/US 99/15481

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	<p>US 5 763 869 A (LAMLA MICHAEL ET AL) 9 June 1998 (1998-06-09)</p> <p>column 3, line 3-34; figures 1,2,6 column 5, line 8-15 -----</p>	<p>1-7,9, 11-18, 20,22-24</p>

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 99/15481

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0291259 A	17-11-1988	JP 63278894 A	16-11-1988
		JP 63278895 A	16-11-1988
		JP 63280694 A	17-11-1988
		CN 1030663 A,B	25-01-1989
		DE 3852907 D	16-03-1995
		DE 3852907 T	24-05-1995
		KR 9700278 B	08-01-1997
		US 4954985 A	04-09-1990
US 4918631 A	17-04-1990	EP 0167044 A	08-01-1986
		US 4749982 A	07-06-1988
US 5763869 A	09-06-1998	DE 4443766 A	13-06-1996
		WO 9618172 A	13-06-1996
		EP 0744060 A	27-11-1996
		JP 9508996 T	09-09-1997
